

PTO-1449	Application No. 10/047,332	Applicant(s) Ee Hong Kwek et al. #10	
	Docket Number 073671.0149	Group Art Unit 2816	Filing Date Jan. 14, 2002

Information Disclosure Citation  
In an Application



## U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A	3,302,138	01/31/1967	H.C. Brown et al.	331	177 V	08/18/1965
B	4,088,968	05/09/1978	Pradal et al.	332	26	06/03/1977
C	5,739,730	04/14/1998	Rotzoll	331	177 V	12/22/1995
D	6,268,778 B1	07/31/2001	Mucke et al.	331	117 R	05/03/1999
E						
F						
G						
H						
I						
J						
K						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
L		06350335	22-12-94	Japan (abstract only)	H03B	5/04	X	
M								
N								
O								

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
P		Parker et al., "A Low-Noise 1.6-GHz CMOS PLL with On-Chip Loop Filter," IEEE, 407-410	1997
Q		Samori et al., "Phase Noise Degradation at High Oscillation Amplitudes in LC-Tuned VCO's," IEEE Journal of Solid-State Circuits, Vol. 35, No. 1, 96-99	01/2000
R		Zannoth et al., "A Single-Chip Bipolar 1.6-GHz VCO with Integrated-Bias Network," IEEE Transactions on Microwave Theory and Techniques, Vol. 48, No. 2, 203-205	02/2000
S		PCT Report for PCT/US 03/00468, 7 pages	May 9, 2003
T			
U			
V			

EXAMINER 	DATE CONSIDERED 9/24/03
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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